

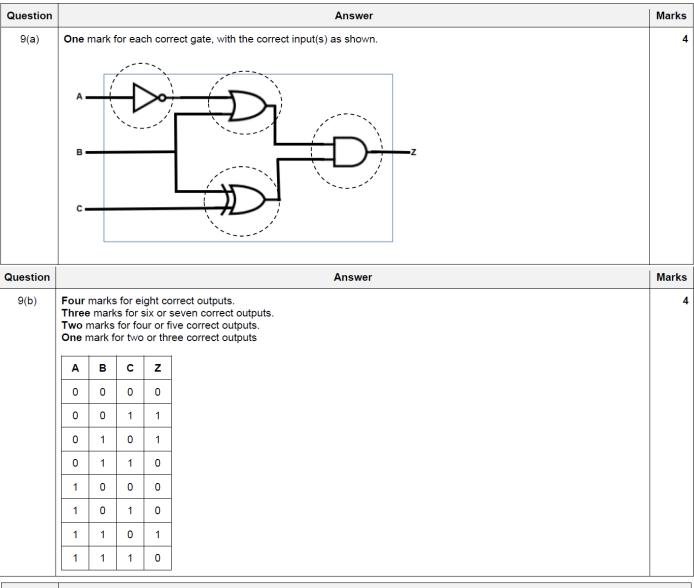
Question	Answer	Marks
7(a)	One mark for each point NOT A AND B OR NOT C expression correct (NOT A AND B) OR NOT C	4

Question				
7(b)	Α	В	С	Х
	0	0	0	1
	0	0	1	0
	0	1	0	1
	0	1	1	1
	1	0	0	1
	1	0	1	0
	1	1	0	1
	1	1	1	0
	4 marks	s for 8 c	correct o	outputs
	2 marks	s for 4/5	correc	t output t output
	1 mark	for 2/3	correct	outputs

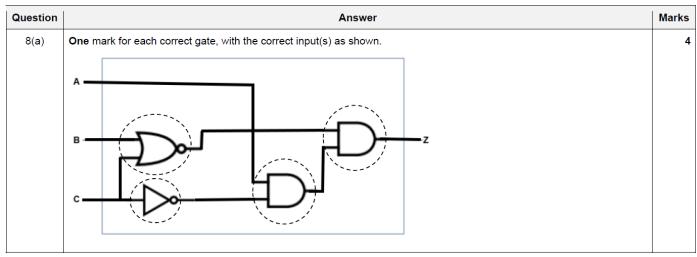
Question					Answer	Marks
9(a)	A — B — C —	mark f	or eac	h corr	ect gate, with the correct input(s) as shown.	4
9(b)	Three Two r	mark marks	s for s for fo	six or s ur or fi	rrect outputs. seven correct outputs. ve correct outputs. ee correct outputs	4
	Α	В	С	Z		
	0	0	0	1		
	0	0	1	1		
	0	1	0	1		
	0	1	1	1		
	1	0	0	1		
	1	0	1	1		
	1	1	0	0		
	1	1	1	1		

Question				Answer	Marks
6(a)	One m	ark for o	correct o	gate and one mark for correct truth table	2
	AND				
	A	В	X		
	0	0	0		
	0	1	0		
	1	0	0		
	1	1	1		
6(b)	One m	ark for o	correct o	gate and one mark for correct truth table	2
	XOR //	EOR			
	A	В	х		
	0	0	0		
	0	1	1		
	1	0	1		
	1	1	0		

Question Marks Answer 6(c) One mark for correct gate and one mark for correct truth table 2 NOR В X Α 0 0 1 0 0 1 1 0 0 1 5 6(d) One mark for each correct gate, with the correct input(s) as shown.



Question				Ansv	ver	Marks
8	4 marks for 8 cc 3 marks for 6/7 2 marks for 4/5 1 mark for 2/3 c	correct outputs correct outputs				
	A	В	С	x		
	0	0	0	0		
	0	0	1	0		
	0	1	0	0		
	0	1	1	0		
	1	0	0	0		
	1	0	1	1		
	11	1	0	0		
	1	1	1	0		



Question					Answer	Mark
	Three Two r	mark marks	s for s	six or s ur or f	orrect outputs. seven correct outputs. five correct outputs. ree correct outputs	
	Α	В	С	z		
	0	0	0	0		
	0	0	1	0		
	0	1	0	0		
	0	1	1	0		
	1	0	0	1		
	1	0	1	0		
	1	1	0	0		
	1	1	1	0		

Answer										
Т	ruth tabl	e	Logic gate		3					
Α	В	Output	NAND	[1]						
0	0	1								
0	1	1								
1	0	1								
1	1	0								
Α	В	Output	XOR / Exclusive OR	[1]						
0	0	0								
0	1	1								
1	0	1								
1	1	0								
Α	В	Output	NOR	[1]						
0	0	1								
0	1	0								
1	0	0								
1	1	0								
	0 0 1 1 A 0 0 1 1 A 0	0 0 0 1 1 1 A B 0 0 1 1 1 1 A B 0 0 0 1 1 1 1 1 A B 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0 0 1 0 1 1 1 0 1 1 1 0 A B Output 0 0 0 0 1 1 1 1 0 A B Output 0 0 1 0 1 0 1 0 0 1 0 0	A B Output 0 0 1 0 1 1 1 0 1 1 1 0 A B Output 0 0 0 0 1 1 1 1 0 0 XOR / Exclusive OR 0 0 1 1 1 0 A B Output 0 0 1 1 1 0 NOR 0 1 1 1 0 0 NOR	A B Output 0 0 1 1 1 1 0 1 1 1 0 A B Output 0 0 0 1 1 1 1 0 XOR / Exclusive OR [1] A B Output 0 0 1 1 1 1 0 1 1 1 0 A B Output 1 1 0 NOR [1] A B Output 0 1 1 1 0 I 0 0 I 0 0 I					

Question	Answer	Marks
5(a)	One mark for each correct logic gate with correct input(s) A B C	6
5(b)	Any one from: NOR	1
	XOR // EOR	

Question					Answer		Marks
5(c)	Four marks for 8 correc Three marks for 6/7 cor Two marks for 4/5 correc One mark for 2/3 correc	rect outp	outs ts				4
		Α	В	С	Working space	х	
		0	0	0		1	
		0	0	1		1	
		0	1	0		1	
		0	1	1		1	
		1	0	0		1	
		1	0	1		1	
		1	1	0		0	
		1	1	1		1	

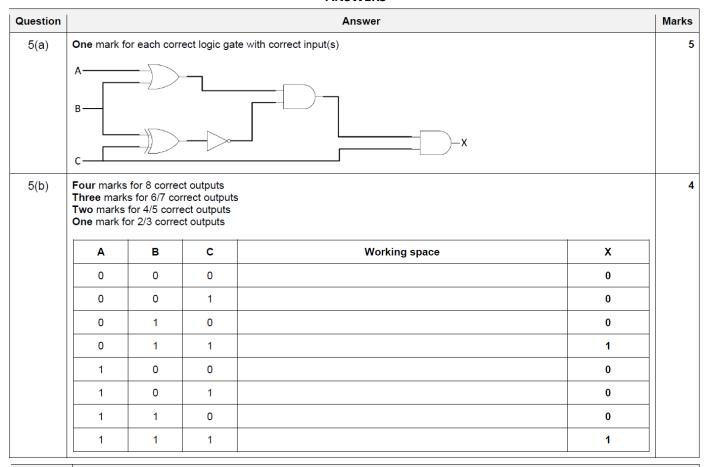
Question	Answer										
7(a)	One mark for each correct row										
				Sta	ement	NAND (✓)	OR (✓)	XOR (✓)			
		if both inputs	s are 1,	the output	s 1		✓				
		if both inputs	s are di	fferent fron	each other, the output is 1	✓	✓	1			
		if both inputs	s are 0,	the output	s 0		✓	✓			
		if both inputs	s are th	e same as	ach other, the output is always 0			✓			
7(b)	One mark for a correct logic gate, one mark for a corresponding truth table										
	• AND	Α	В	Output							
		^	_ B	Output							
		0	0	0							
		0	1	0							
		1	0	0							

Question	Answer	Marks
5(a)	One mark for each correct logic gate with the correct inputs	6

Question					Answer		Marks
5(b)	Four marks for 8 corre Three marks for 6/7 co Two marks for 4/5 corre One mark for 2/3 corre	rrect out	puts uts				4
		Α	В	С	Working space	X	
		0	0	0		1	
		0	0	1		0	
		0	1	0		1	
		0	1	1		1	
		1	0	0		0	
		1	0	1		1	
		1	1	0		0	
		1	1	1		0	

Question	Answer	Marks
8(a)	One mark for each correct logic gate with correct input(s)	6

Question					Answer	Marks
8(b)	Four marks Three mark Two marks One mark fo	s for 6/ for 4/5	/7 correct	t outputs outputs		4
		Α	В	С	Working space X	
		0	0	0	0	
		0	0	1	1	
		0	1	0	0	
		0	1	1	1	
		1	0	0	0	
		1	0	1	1	
		1	1	0	0	
		1	1	1	1	



Question	Answer									
3(a)	1 mark per row									
		Inputs	AND	OR	NAND	NOR	XOR			
		A = 1 B = 1	✓	✓						
		A = 0 B = 0			✓	✓				
		A = 1 B = 0		✓	✓		✓			
3(b)	1 mark per gate A B C A XOR B NOT C (NOT C) OR B AND	> -	—				— x		4	

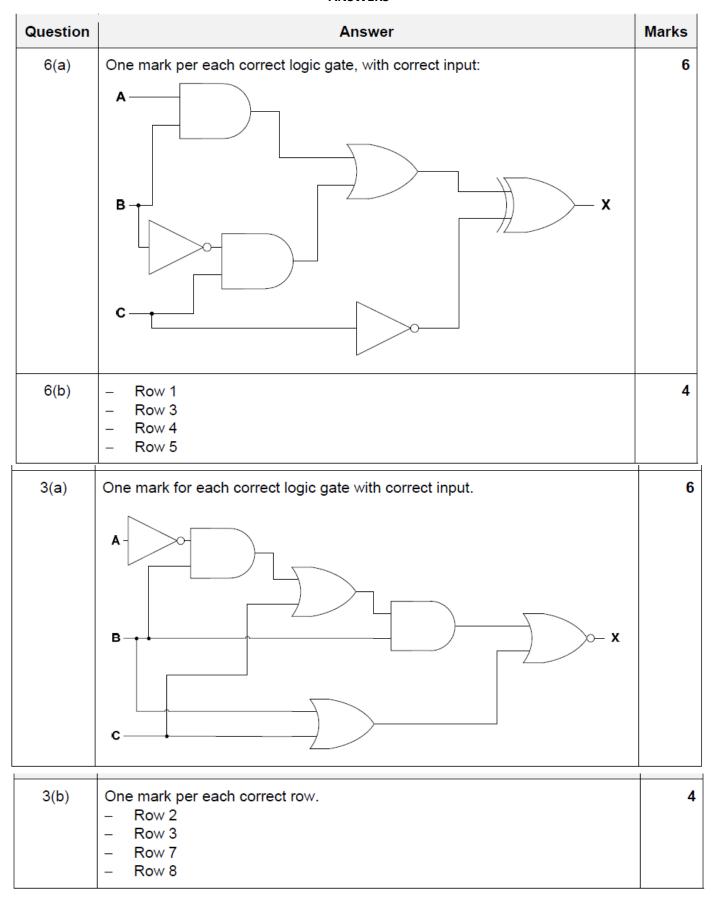
Question	Answer	Marks						
10(a)	One mark per each correct logic gate with the correct input(s).							
	B C							
10(b)	One mark per logic gate name and one mark per correct drawing.	2						
	- NAND							
	- NOR							

Question				Answer		Marks
10(c)	Α	В	С	Working space	X	4
	0	0	0		0	
	0	0	1		0	
	0	1	0		0	
	0	1	1		1	
	1	0	0		0	
	1	0	1		1	
	1	1	0		0	
	1	1	1		1	
	3 marks 2 marks	s per 6/ s per 4/	7 corre 5 corre	outputs ct outputs ct outputs t outputs		

Question	Answer	Marks
11(a)	One mark per each correct logic gate with correct input(s)	5
	B C X	

Question					Answer	Marks			
11(b)	4 marks for 8 correct outputs 3 marks for 6/7 correct outputs 2 marks for 4/5 correct outputs 1 mark for 2/3 correct outputs								
		Α	В	С	Working space X				
		0	0	0	1				
		0	0	1	1				
		0	1	0	1				
		0	1	1	1				
		1	0	0	1				
		1	0	1	1				
		1	1	0	1				
		1	1	1	0				
11(c)	- NOR - XOR / EOR	•	•	· -		2			

Question				Answer	Marks				
8(a)		One mark per each correct logic gate with correct inputs							
	A X								
8(b)	4 marks for 8 correct outputs 3 marks for 6/7 correct outputs 2 marks for 4/5 correct outputs 1 mark for 2/3 correct outputs								
	Α	В	С	Working space X					
	0	0	0	1					
	0	0	1	0					
	0	1	0	1					
	0	1	1	1					
	1	0	0	1					
	1	0	1	0					
	1	1	0	1					
	1	1	1	1					



Question	Answer	Marks
8(a)	ANDNORXOR	3
8(b)	 Row 1 Row 4 Row 7 Row 8 	4

