

## Comp Sci – Data Transmission & Error Checking - ANSWERS

Question	Answer	Marks
3(a)	1 mark for: serial  Any <b>two</b> from: serial data transmission more reliable over distance less likely for the data to be skewed/out of synchronisation less interference as only a single wire it is a cheaper connection as only single wire needed // cheaper to set up	<b>3</b>
3(b)	Register 1 – odd Register 2 – even	<b>2</b>
3(c)	Any <b>one</b> from: checksum ARQ (Automatic Repeat request)	<b>1</b>

### 1 (a) parallel

any **one** from:

- 8 bits/1 byte/multiple bits sent at a time
- using many/multiple/8 wires/lines (1 mark)

#### serial

any **one** from:

- one bit sent at a time
- over a single wire (1 mark) [2]

### (b) parallel

- faster rate of data transmission (1 mark)

#### serial

any **one** from:

- more accurate/fewer errors over a longer distance
- less expensive wiring
- less chance of data being skewed/out of synchronisation/order (1 mark) [2]

### (c) parallel

any **one** from:

- sending data from a computer to a printer
- internal data transfer (buses) (1 mark)

#### serial

- connect computer to a modem (1 mark) [2]

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- 2 (a) – universal serial bus  
– description of USB [1]

(b) Any **two** from:

- devices are automatically detected and configured when initially attached
  - impossible to connect device incorrectly/connector only fits one way
  - has become the industry standard
  - supports multiple data transmission speeds
  - lots of support base for USB software developers
  - supported by many operating systems
  - backward compatible
  - faster transmission compared to wireless
- [2]

- 5 (a) 1 mark per correctly placed tick

Received byte	Byte transmitted correctly	Byte transmitted incorrectly
1 1 0 0 1 0 0 0		✓
0 1 1 1 1 1 0 0		✓
0 1 1 0 1 0 0 1	✓	

[3]

- (b) (i) byte number: 7  
column number: 6

[2]

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(ii) Any **two** from:

- letter "A"(byte 7) transmitted as odd parity (three 1s)
- column 6 has odd parity (seven 1s)
- intersection of byte 7 and column 6 indicates incorrect bit value

[2]

(c) **190**

[1]

(d) Any **one** from:

- 2 bits interchanged (e.g.  $1 \rightarrow 0$  and  $0 \rightarrow 1$ ) that won't change parity value
- even number of bits/digits are transposed
- If there are multiple errors in the same byte/column, that still produce the same parity bit, the error will not be detected

[1]

9 (a) **1** mark for correct check digit and **1** mark for showing the calculation

$$(4 \times 1) + (2 \times 2) + (4 \times 3) + (1 \times 4) + (5 \times 5) + (0 \times 6) + (8 \times 7)$$

$$= 4 + 4 + 12 + 4 + 25 + 0 + 56 = 105$$

$$105/11 = 9 \text{ remainder } 6$$

check digit is: **6**

} 1 mark for any correct  
line of working

[2]

(b) **incorrect** check digit

[1]

- check digit should be 1
- $(3 \times 1) + (2 \times 2) + (4 \times 3) + (0 \times 4) + (0 \times 5) + (4 \times 6) + (5 \times 7) // 3 + 4 + 12 + 0 + 0 + 24 + 35 //$   
Total = 78
- $78/11$  gives 7 remainder 1

[2]

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6 (a)

Type	Tick (✓)
simplex	
half-duplex	
full-duplex	✓

Method	Tick (✓)
serial	
parallel	✓

Type	Tick (✓)
simplex	✓
half-duplex	
full-duplex	

Method	Tick (✓)
serial	✓
parallel	

Type	Tick (✓)
simplex	
half-duplex	✓
full-duplex	

Method	Tick (✓)
serial	✓
parallel	

[6]

(b) Any **two** from:

- single wire means there is less chance of interference/data corruption
- single wire reduces costs
- more reliable over greater distances
- bits will still be synchronised after transmission

[2]

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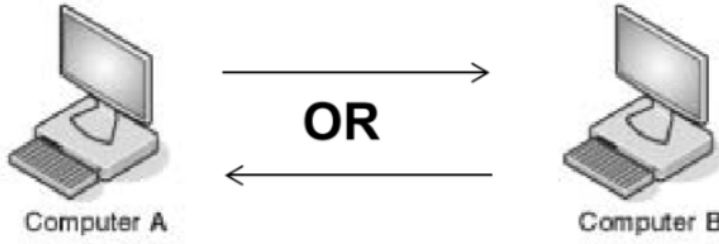
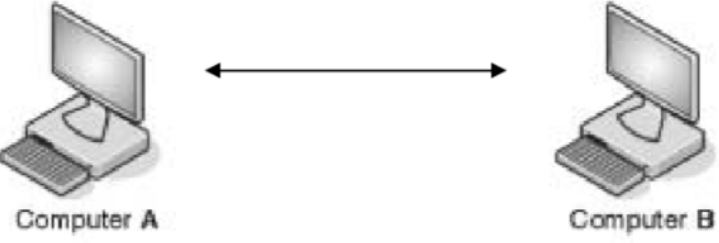
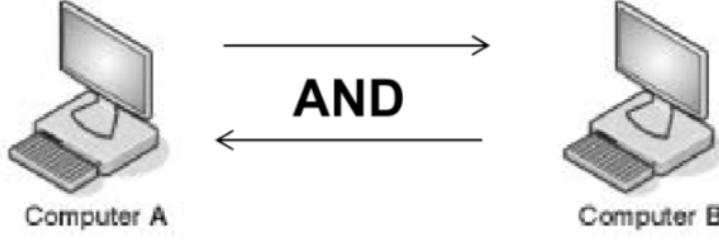
Question	Answer	Marks																		
4	<p>1 mark per correct tick</p> <table border="1" data-bbox="428 317 1192 806"> <thead> <tr> <th data-bbox="428 317 980 363">Statement</th> <th data-bbox="980 317 1084 363">True</th> <th data-bbox="1084 317 1192 363">False</th> </tr> </thead> <tbody> <tr> <td data-bbox="428 363 980 443">Data is transmitted in one direction only, one bit at a time.</td> <td data-bbox="980 363 1084 443"></td> <td data-bbox="1084 363 1192 443">✓</td> </tr> <tr> <td data-bbox="428 443 980 522">Data is transmitted in both directions, multiple bits at a time.</td> <td data-bbox="980 443 1084 522"></td> <td data-bbox="1084 443 1192 522">✓</td> </tr> <tr> <td data-bbox="428 522 980 602">Data is transmitted in one direction only, multiple bits at a time.</td> <td data-bbox="980 522 1084 602"></td> <td data-bbox="1084 522 1192 602">✓</td> </tr> <tr> <td data-bbox="428 602 980 703">Data is transmitted in both directions, but only one direction at a time. Data is transmitted one bit at a time.</td> <td data-bbox="980 602 1084 703">✓</td> <td data-bbox="1084 602 1192 703"></td> </tr> <tr> <td data-bbox="428 703 980 806">Data is transmitted in both directions, but only one direction at a time. Data is transmitted multiple bits at a time.</td> <td data-bbox="980 703 1084 806"></td> <td data-bbox="1084 703 1192 806">✓</td> </tr> </tbody> </table>	Statement	True	False	Data is transmitted in one direction only, one bit at a time.		✓	Data is transmitted in both directions, multiple bits at a time.		✓	Data is transmitted in one direction only, multiple bits at a time.		✓	Data is transmitted in both directions, but only one direction at a time. Data is transmitted one bit at a time.	✓		Data is transmitted in both directions, but only one direction at a time. Data is transmitted multiple bits at a time.		✓	5
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**Comp Sci – Data Transmission & Error Checking - ANSWERS**

Question	Answer	Marks												
5(a)	<p>1 mark per correct tick</p> <table border="1" data-bbox="415 317 1154 583"> <thead> <tr> <th data-bbox="415 317 756 449">Received byte</th> <th data-bbox="756 317 951 449">corrupted during transmission (✓)</th> <th data-bbox="951 317 1154 449">not corrupted during transmission (✓)</th> </tr> </thead> <tbody> <tr> <td data-bbox="415 449 756 493">10110100</td> <td data-bbox="756 449 951 493">✓</td> <td data-bbox="951 449 1154 493"></td> </tr> <tr> <td data-bbox="415 493 756 537">01101101</td> <td data-bbox="756 493 951 537"></td> <td data-bbox="951 493 1154 537">✓</td> </tr> <tr> <td data-bbox="415 537 756 583">10000001</td> <td data-bbox="756 537 951 583">✓</td> <td data-bbox="951 537 1154 583"></td> </tr> </tbody> </table>	Received byte	corrupted during transmission (✓)	not corrupted during transmission (✓)	10110100	✓		01101101		✓	10000001	✓		3
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10110100	✓													
01101101		✓												
10000001	✓													
5(b)	<p><b>Four</b> from:</p> <ul style="list-style-type: none"> <li>Uses acknowledgement and time out</li> <li>Check performed on received data // error is detected by e.g. parity check, check sum</li> <li>If error detected, request sent to resend data // negative acknowledgment is used</li> <li>If no acknowledgement is sent that data is received // positive acknowledgement is used</li> <li>Data is resent / Resend request repeated, till data is resent correctly ...</li> <li>... or request times out // limit is reached</li> </ul>	4												

**Comp Sci – Data Transmission & Error Checking - ANSWERS**

Question	Answer	Marks
4	<p><b>Two marks for each correct description</b></p> <p><b>Parity Check</b>            Checks a byte of data            Check is performed when data is received            A parity bit is added (to the parity byte)            Counts / checks number of 1's // counts / checks to see if 1's are even            // counts / checks to see if 1's are odd            Can be <u>even</u> or <u>odd</u>            If parity is incorrect, error is detected</p> <p><b>Check digit</b>            A digit that is calculated from the data // uses modulo to calculate digit            // valid description of modulo            It is appended / added to the data            Digit is recalculated when data is entered            Digits are compared to check for error</p> <p><b>Checksum</b>            A value is calculated from the data // Valid description of calculation            It is transmitted with the data            Value is recalculated after transmission            Values are compared after transmission to check for error</p> <p><b>Automatic Repeat reQuest</b>            Uses acknowledgement / request and time-out            Error control protocol            Check performed on receiving data // error is detected by e.g. parity check, check sum            If error detected, request is sent to resend data // negative acknowledgement is used            Resend request is repeated till data is sent correctly / requests time out / limit is reached            Send acknowledgement that data is received // positive acknowledgement is used            If acknowledgement not received in set time data is resent</p>	8

Question	Answer	Marks
7(a)	<p>1 mark for correct arrow(s), one mark for correct description</p> <p style="text-align: center;"><b>Simplex data transmission</b></p>  <p>(Direction of data is) one way only // unidirectional</p> <p style="text-align: center;"><b>Duplex data transmission</b></p>  <p>(Direction of data is both ways) <u>at same time / simultaneously / concurrently</u></p> <p style="text-align: center;"><b>Half-duplex data transmission</b></p>  <p>(Direction of data is both ways) but at different times / <u>not at the same time / not simultaneously / not concurrently</u></p>	6

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Question	Answer	Marks
7(b)	<p>1 mark each use, must be different.</p> <p><b>Simplex</b> e.g.:</p> <p>Microphone to computer                      Sensor to computer                      Computer to printer                      Computer to speaker                      Computer to monitor                      Webcam to computer                      Sending data to a device // sending data from a device</p> <p><b>Duplex</b> e.g.:</p> <p>Telephone call                      Voice over IP                      Computer to printer (only award once)                      Instant messaging                      Broadband connections                      Video conferencing                      Sending data to and from devices e.g wireless technology                      Computer to modem</p>	<b>2</b>
7(c)	<p>2 marks for IC, 2 marks for USB</p> <p><b>IC</b></p> <p>parallel transmission // description of parallel for sending data internally</p> <p><b>USB</b></p> <p>serial transmission // description of serial for sending data externally (to and from peripherals / between devices)</p>	<b>4</b>

Question	Answer	Marks																
4(a)(i)	<table border="1"> <thead> <tr> <th>Method 1</th> <th>Tick (✓)</th> <th>Method 2</th> <th>Tick (✓)</th> </tr> </thead> <tbody> <tr> <td>Serial</td> <td align="center">✓</td> <td>Simplex</td> <td></td> </tr> <tr> <td>Parallel</td> <td></td> <td>Half-duplex</td> <td></td> </tr> <tr> <td></td> <td></td> <td>Duplex</td> <td align="center">✓</td> </tr> </tbody> </table>	Method 1	Tick (✓)	Method 2	Tick (✓)	Serial	✓	Simplex		Parallel		Half-duplex				Duplex	✓	<b>2</b>
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4(a)(ii)	<p>Any <b>four</b> from (<b>Max 3</b> for serial):</p> <p>Serial has <u>less/lower</u> interference                      Serial is (more) reliable/accurate <u>over distances</u>                      In serial the bits won't be skewed                      In serial it is easier to collate the bits together again after transmission</p> <p>Duplex transmits data in both directions <u>at the same time</u>                      simplex/half-duplex/remaining methods won't allow read and write at same time</p>	<b>4</b>																

## Comp Sci – Data Transmission & Error Checking - ANSWERS

Question	Answer	Marks
4(b)	<p>1 mark for error checking method, 2 marks for description:</p> <p><b>Checksum</b>                      A value is calculated from the data // Description of calculation                      Value is transmitted with data                      Value is recalculated after transmission                      If the values match the data is (more likely to be) accurate</p> <p><b>Parity check</b>                      A parity bit is transmitted with each byte of data                      Odd or even (parity can be used)                      Counts / checks number of 1's // counts / checks to see if 1's are even // counts / checks to see if 1's are odd                      (Each byte is) checked after transmission to see if it matches the odd/even parity used</p> <p><b>Automatic Repeat Request (ARQ)</b>                      Uses acknowledgement and timeout                      When a device detects an error in data transmission it asks for the packet to be resent / no error detected, positive acknowledgment sent                      The sending device resends the packet after the request to resend/ timeout received                      This process is continuous until the packet received is correct/until the ARQ limit is reached</p> <p><b>Echo (check)</b>                      Copy of data is sent back to sender                      Data is compared to see if it matches                      If it does not match error detected</p>	<b>6</b>

**12 (a)**

1	1	1	1	1	0	0	0
0	0	0	0	0	1	1	1

[2]

**(b)** 1 mark for error detection method and 1 mark for description

- Check sum
- ... sum of bits is transmitted and checked against the sum of the received bits
  
- Check digit
- ... a digit that is calculated (e.g. using modulo-11) and transmitted with the data
  
- ARQ
- ... when an error is detected in a packet of data a request is automatically sent for the data to be resent

[2]

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7 (a) (i) 1 mark for correct check digit and 1 mark for showing the calculation

$$(4 \times 1) + (2 \times 2) + (4 \times 3) + (1 \times 4) + (5 \times 5) + (0 \times 6) + (8 \times 7)$$

$$= 4 + 4 + 12 + 4 + 25 + 0 + 56 = 105$$

$$105/11 = 9 \text{ remainder } 6$$

check digit is: **6**

[2]

(ii) **1 mark**

– No/incorrect check digit

**2 marks**

– Total is 78

– 78/11 ...

– ... gives 7 remainder 1

– check digit should be 1

[3]

(b) (i) 1 mark for each correct parity bit

parity bit

<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>
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parity bit

<b>1</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>1</b>
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[2]

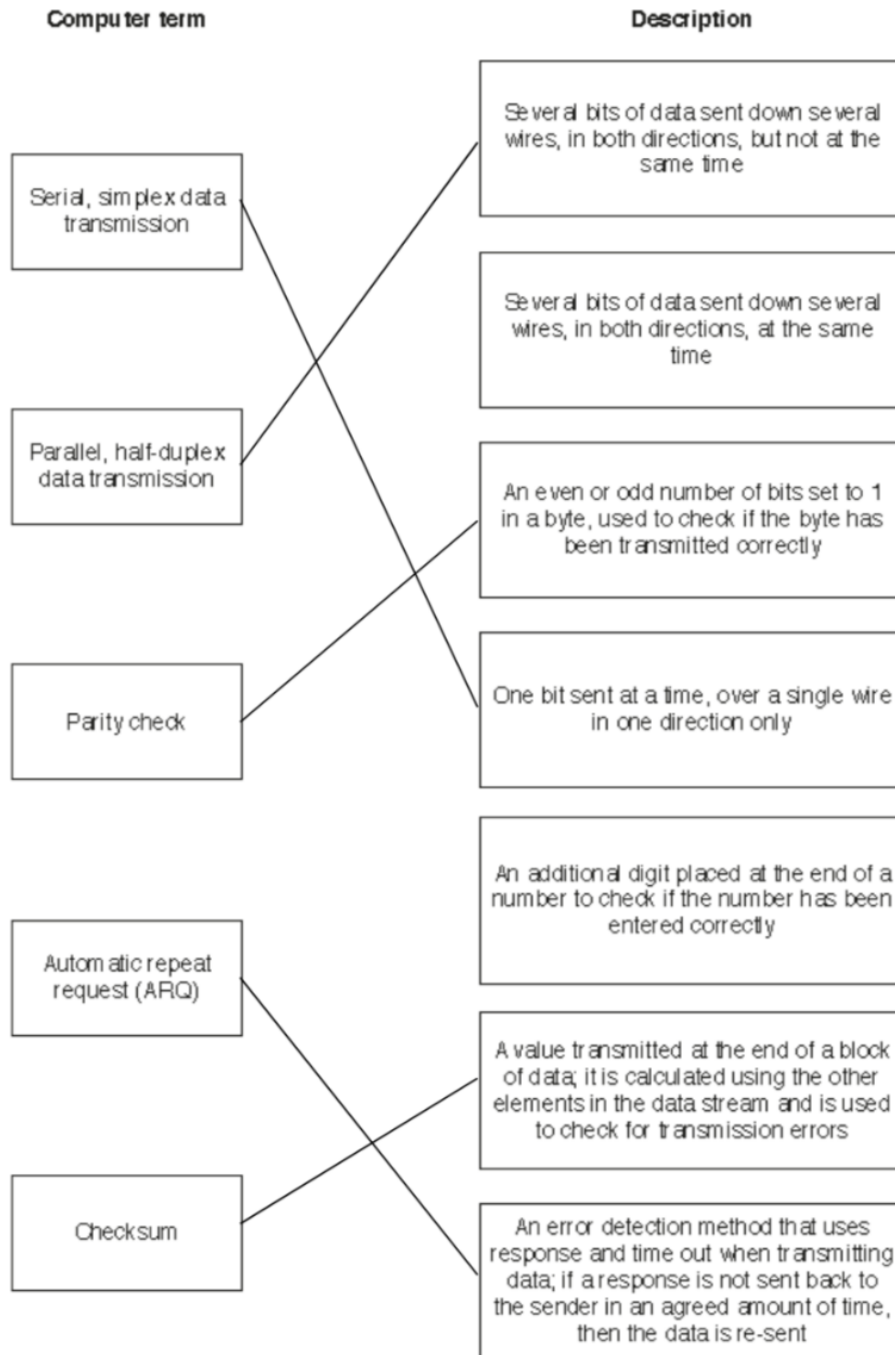
(ii) Any **one** from:

– an even number of digits are changed

– a transposition error(s) has occurred

[1]

3



[5]

## Comp Sci – Data Transmission & Error Checking - ANSWERS

3 (a) (i) Any **two** from:

serial

- one bit sent at a time // bits sent sequentially
- over a single wire
- synchronous or asynchronous

[2]

(ii) Any **two** from:

parallel

- several bits / a byte sent at a time
- using many / multiple wires
- synchronous

[2]

(b) – serial

Any **two** from:

- serial data transmission more reliable over long distances
- less likely for the data to be skewed/out of synchronisation
- less interference as only a single wire
- it is a cheaper connection as only single wire needed // cheaper to set up
- a fast connection is not required as a printer is limited by its printing speed

[3]

4 (a) Intersection of Row 7 and column 4 circled

[1]

- (b) – Row (byte number) 7 has an odd number of 1s (five 1s)  
– Column (bit number) 4 has an odd number of 1s (five 1s)

[2]